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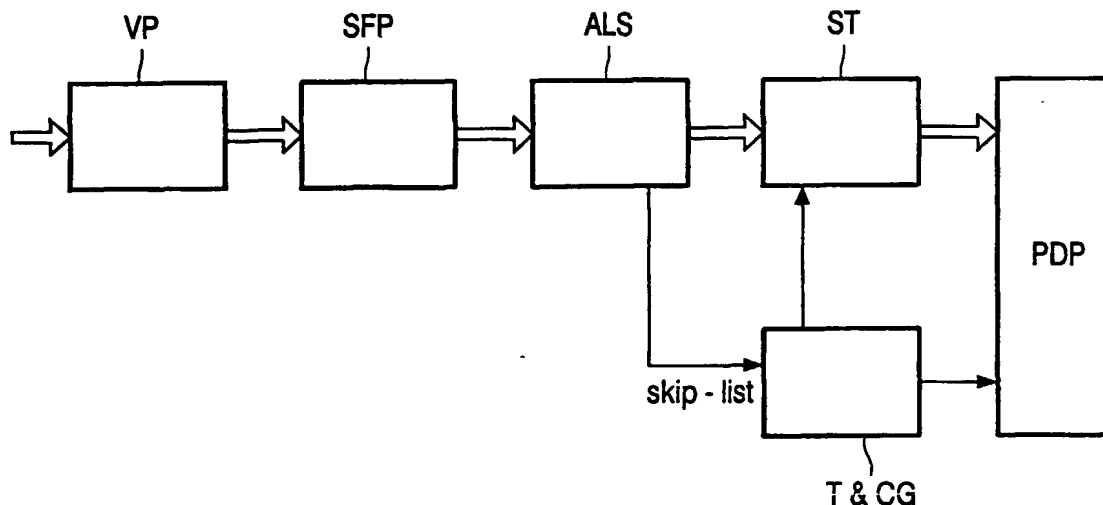
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(54) Title: ADDRESSING CELLS OF A DISPLAY PANEL



(57) Abstract: The present invention relates to a method and a device for addressing cells of a display panel, in particular discharge cells of a plasma display panel (PDP), each cell corresponding to a pixel in response to a video signal, wherein the addressing of those cells which are identified as being not active is skipped by an adaptive line skip unit (ALS).

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INTERNATIONAL SEARCH REPORT

 Internal Application No
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A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 G09G3/28

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G09G

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

WPI Data, PAJ, EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	HOPPENBROUWERS J ET AL: "ADDRESS TIME REDUCTION IN PDPS BY MEANS OF PARTIAL LINE DOUBLING" 2001 SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS. SAN JOSE, CA, JUNE 5 - 7, 2001, SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS, SAN JOSE, CA: SID, US, vol. 32, June 2001 (2001-06), pages 1142-1145, XP001054124 the whole document --- -/-	1,5

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INTERNATIONAL SEARCH REPORT

Internat Application No
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